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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Re Application of:

Inventor: Bruno DEVOS et al.

Application No.: 10/691,635

Confirmation No: 7636

Filed: October 24, 2003

Attorney No.: DEVO3002/JEK/JJC

Customer No.: 23364

Examiner: Unassigned

Art Unit: 2673

For: A PIXEL MODULE FOR USE IN A LARGE AREA DISPLAY

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Pursuant to Rule 37 C.F.R. §§ 1.51(b), 1.56, 1.97 and 1.98, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each foreign patent or publication listed is submitted herewith. A copy of any U.S. patent or published patent application identified herein is not submitted herewith since this application was filed or entered the U.S. national stage after June 30, 2003.

**Additional Documents or Information Not Listed
On Accompanying Form PTO 1449 Submitted for Consideration**

European Search Report dated May 18, 2004 in the European counterpart application EP 03 07 7824.

Concise Statement of Relevancy
(Non-English Language Documents Only)

All of the documents are mentioned in the European Search Report and the degree of relevancy of each document is evident in view of the Search Report. All of the documents are in the English language.

Certification

This Information Disclosure Statement is submitted prior to the mailing date of the first Office Action on the merits received by Applicant in the above-identified application, and therefore no fee is necessary.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

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Respectfully submitted,



Date: July 12, 2004

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B/O Form PTO-1449 U.S. Department of Commerce Patent and Trademark Office Information Disclosure Statement by Applicant	Atty. Docket Number DEVO3002/JEK/JJC	Serial Number 10/691,635
	Applicant Bruno DEVOS et al.	
	Filing Date October 24, 2003	Group 2673

U.S. Patent Documents

Examiner Initial	Document Number	Date	Patentee/Applicant	Class	Subclass	Filing Date if Appropriate
	5,184,114	Feb. 1993	Brown			
	5,513,334	Apr. 1996	Alexander			
	5,990,802	Nov. 1999	Maskeny			
	6,020,868	Feb. 2000	Greene et al.			

Foreign Patent Documents

Examiner Initial	Document Number	Publication Date	Country/Agency	Class	Subclass	Translation	
						Yes	No
	1 052 614 A2	Nov. 2000	EP				
	1 071 067 A2	Jan. 2001	EP				
	99/41732 A2	Aug. 1999	WO				
	01/93242 A1	Dec. 2001	WO				

Other Documents (Including Author, Title, Date, Pertinent Pages, Place of Publication, Etc.)

	ALLEGRO MICROSYSTEM: "5821 and 5822 BiMOS II 8 bit serial input latched drivers", 2000, Allegro Microsystem, Worcester, Massachusetts XP002268893.

Examiner	Date Considered
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EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; Draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.